

#11/B 4/30/01 Hayes

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re Application of | |) | Group Art Unit: 2825 | | | |
|---|----------------------|----|----------------------|-------------------|-------------|----------|
| David C. Chapman | • |) | Examiner: A | . Thomps | son | |
| Serial No. 09/421,437 | |)) | | | | |
| Filed: October 19, 1999 | |) | | | | |
| For: APPROACH FOR | ROUTING AN INTEGRATE | ED | CIRCUIT | | | |
| Box Amendment Commissioner for Patents Washington, D.C. 20231 | AMENDMENT AND RESP | Oì | NSE | TC 2800 MAIL RUUF | APR 26 2001 | RECEIVED |
| Sir. | | | | 3 | | |

In response to the Office Action mailed February 9, 2001, please amend the application referenced above as indicated hereinafter. Pursuant to updated rule 37 C.F.R. § 1.121, the amended and new claims are provided in "clean form" below and a complete set of marked up claims showing the deletions and additions in amended claims and the new claims is provided on separate pages after this amendment and response.

1 11. (AMENDED) The method as recited in Claim 1, wherein determining the routing
path between the first and second integrated circuit devices includes
identifying one or more obstacles that block the routing path,
determining one or more locations to employ corner clipping to provide additional
space for the routing path, and

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